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MAR 01 2010

Docket No.: R2184.0492/P492
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Toshihisa Nagata et al.

Application No.: 10/577,666

Confirmation No.: 8451

Filed: May 1, 2006

Art Unit: 2838

For: SEMICONDUCTOR DEVICE AND VOLTAGE
REGULATOR, WITH FORCE BALANCED
MONITOR TRANSISTOR CONTAINING
PLURAL TRANSISTORS

Examiner: Harry R. Behn

AMENDMENT AFTER FINAL

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

Responsive to the Office Action dated November 24, 2009, please amend the above-referenced application as follows:

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.

DSMDB-2759504v01